

First Named Inventor	Frankie F. Roohparvar	INFORMATION DISCLOSURE STATEMENT
Serial No.	Unknown	
Filing Date	Filed herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Confirmation No.	Unknown	
Attorney Docket No.	400.045US02	

Title: STATUS REGISTER TO IMPROVE INITIALIZATION OF A
SYNCHRONOUS MEMORY

Commissioner for Patents
M.S.: Patent Application
P. O. Box 1450
Alexandria, VA 22313-1450

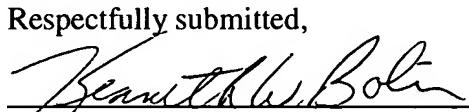
In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

Further pursuant to MPEP §609, because all of the references listed on the attached Form 1449 have been previously submitted and made of record in the parent application, U.S. Patent Application 09/608,257, filed June 30, 2000, copies of the references previously made of record in the parent application are not submitted herewith.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Date: 3/22/04

Respectfully submitted,



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T - 612/312-2200
F - 612/312-2250

Applicant(s)	Frankie Roohparvar	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
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U.S. Patent References				
Examiner Initials	Document No.	Publication/ Issue Date	Name	Filing Date
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Foreign Patent References				
Examiner Initials	Foreign Patent		Name	Publication Date
	Country	No.		

Examiner Signature		Date Considered
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		

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Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
	Keeth, et al., "DRAM circuit design: a tutorial," IEEE Press, 2001, pp.16-23, 142-153
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	Micron, "16 Mb: x16 SDRAM" <u>Synchronous DRAM</u> , www.micron.com, copyright 1999 Micron Technology, Inc., pp. 1-51

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Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce